



M.TECH I SEMESTER (R19 REGULATION) REGULAR/SUPPLEMENTARY EXAMINATIONS, JULY/AUGUST - 2021 T I M E T A B L E

TIME: 02:00 PM TO 05:00 PM

BRANCH & SPECIALIZATION	27.07.2021 (Tuesday)	29.07.2021 (Thursday)	03.08.2021 (Tuesday)	05.08.2021 (Thursday)	07.08.2021 (Saturday)
CSE Computer Science (58)	Advanced Data Structures & Algorithms (M5802)	Mathematical Foundations of Computer Science (M5801)	Elective - II Object oriented Software Engineering (M5808)	Elective - I Advanced Operating Systems (M5805)	Research Methodology and IPR (M0109)
ECE VLSI&EMBEDED SYSTEMS (68)	Microcontrollers and Programmable Digital Signal Processors (M5502)	RTL Simulation and Synthesis with PLDs (M6801)	Elective-II Programming Languages for Embedded Systems (M5506)	Elective-I Digital Signal and Image Processing (M5503)	Research Methodology and IPR (M0109)

PRINCIPAL



**NARASARAOPETA
INSTITUTE OF TECHNOLOGY**

Approved by AICTE, New Delhi, Affiliated to JNTU Kakinada

MOTIVATE
INNOVATE
EMPOWER



M.TECH I SEMESTER (R16 REGULATION) REGULAR/SUPPLEMENTARY EXAMINATIONS, JULY/AUGUST - 2021

T I M E T A B L E

TIME: 02:00 PM TO 05:00 PM

BRANCH & SPECIALIZATION	27.07.2021 (Tuesday)	29.07.2021 (Thursday)	31.07.2021 (Saturday)	03.08.2021 (Tuesday)	05.08.2021 (Thursday)	07.08.2021 (Saturday)	10.08.2021 (Tuesday)	12.08.2021 (Thursday)
CSE Computer Science (58)	Advanced Data Structures and Algorithm Analysis (I4001)	Mathematical Foundations of Computer Science (I5801)	Computer Organization and Architecture (I5802)	Data Base Management Systems (I5803)	Data Warehousing and Data mining (I0505)	Advanced Operating System (I0504)
ECE VLSI&EMBEDED SYSTEMS (68)	VLSI Technology and Design (I6802)	CMOS Analog IC Design (I6803)	Elective-I CMOS Digital IC Design (I6806)	Digital System Design (I6801)	Elective-II System on Chip Design (I6810)	Hardware Software Co- Design (I6804)

PRINCIPAL